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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 09/543,247 04/05/2000 ABN  
 which is a CON of 08/981,702 03/31/1998 ABN  
 which is a 371 of PCT/JP96/01886 07/08/1996

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 7-171154 07/06/1995

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 04/12/2001

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 2	TOTAL CLAIMS 34	INDEPENDENT CLAIMS 7
Verified and Acknowledged	Examiner's Signature  Initials				

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## TITLE

Semiconductor device and process for fabrication thereof

☐ All Fees  
☐ 1.16 Fees ( Filing )